PRACTICAL – 1

PROGRAM -1

AIM- Design Logical GATES Using LOGISIM

1. OR GATE

OUTPUT:-

|  |
| --- |
|  |

1. NOT GATE

OUTPUT:-

|  |
| --- |
|  |

1. AND GATE

OUTPUT:-

|  |
| --- |
|  |

1. NAND GATE

OUTPUT:-

|  |
| --- |
|  |

1. NOR GATE

OUTPUT:-

|  |
| --- |
|  |

1. XOR GATE

OUTPUT:-

|  |
| --- |
|  |

1. XNOR GATE

OUTPUT:-

|  |
| --- |
|  |